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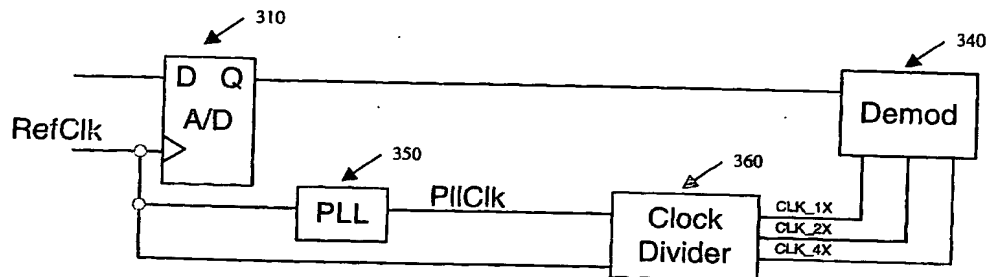
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(54) Title: A METHOD FOR USING A SYNCHRONOUS SAMPLING DESIGN IN A FIXED-RATE SAMPLING MODE

300



(57) Abstract: The present application generally relates to apparatuses such as television signal processing apparatus that process radio frequency signals. More specifically, the present application is particularly useful in integrated circuits that must combine circuitry operating in a synchronous-sampling mode that must be adapted for use with a fixed rate sampling mode application. According to an exemplary embodiment, the television signal processing apparatus 100 comprises a source of a fixed rate digital signal 220, a signal processing circuitry 240 operating in a synchronous-sampling mode wherein the signal processing circuitry 240 comprises a signal representing a symbol rate, and an interpolator 230 for processing the fixed rate digital signal to yield samples at the symbol rate.